

HIGH-VOLTAGE MOS TRANSISTOR AND METHOD FOR FABRICATING THE SAME

SHEET 1 of 7

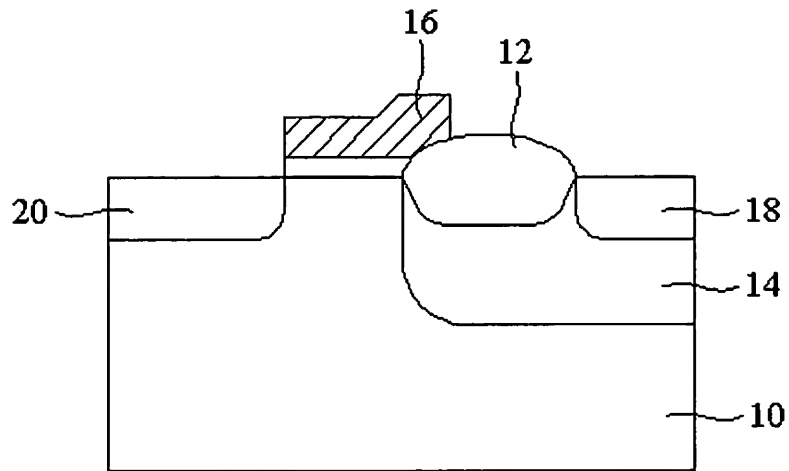


FIG. 1a (PRIOR ART)

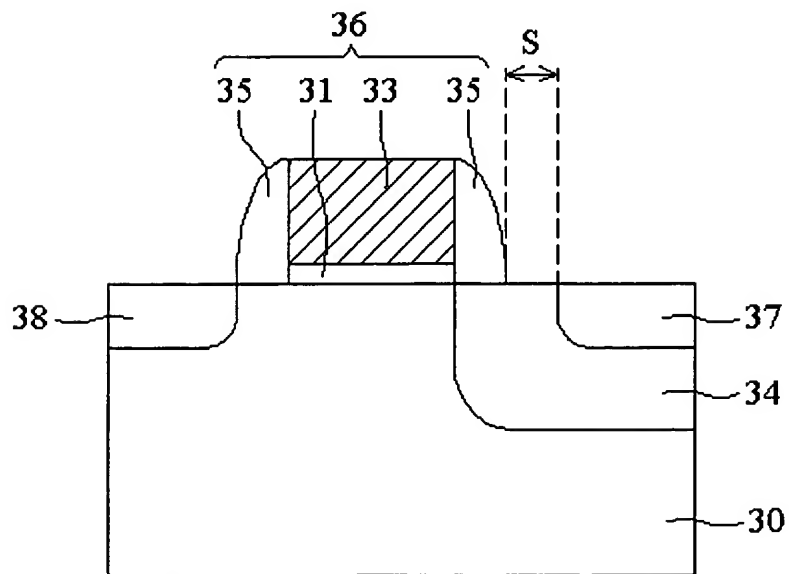


FIG. 1b (PRIOR ART)

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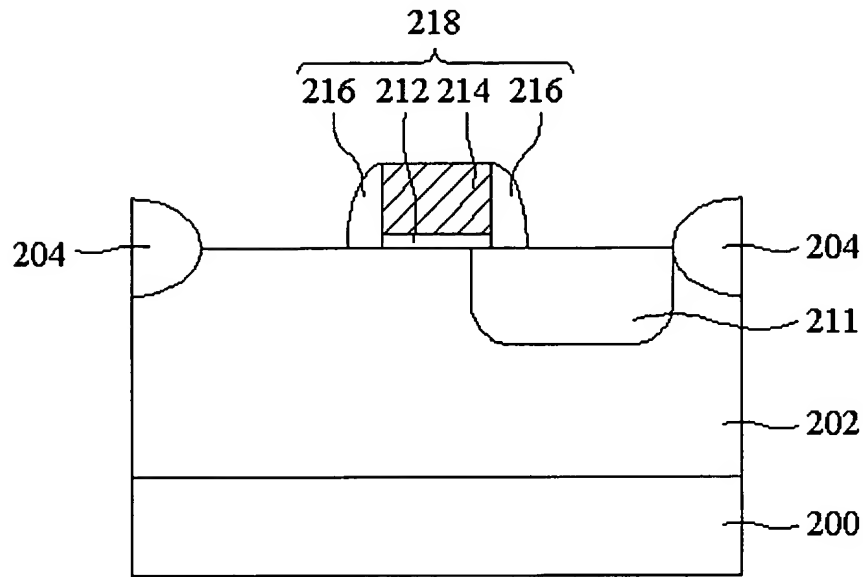


FIG. 2c

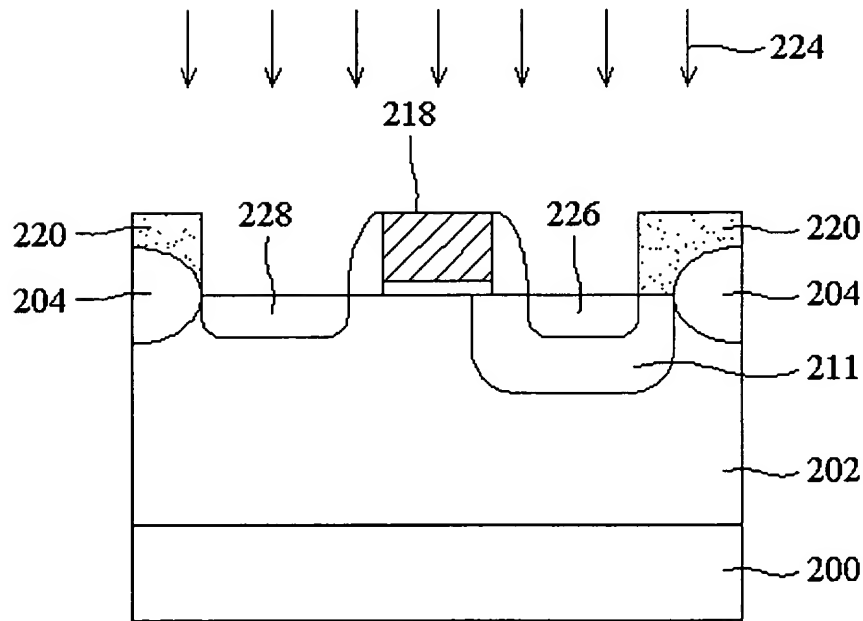


FIG. 2d

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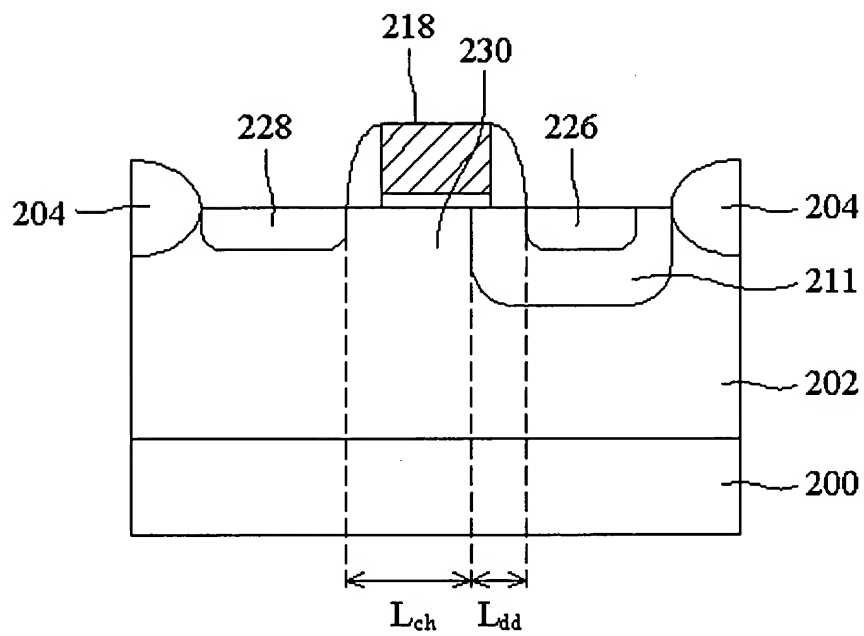


FIG. 2e

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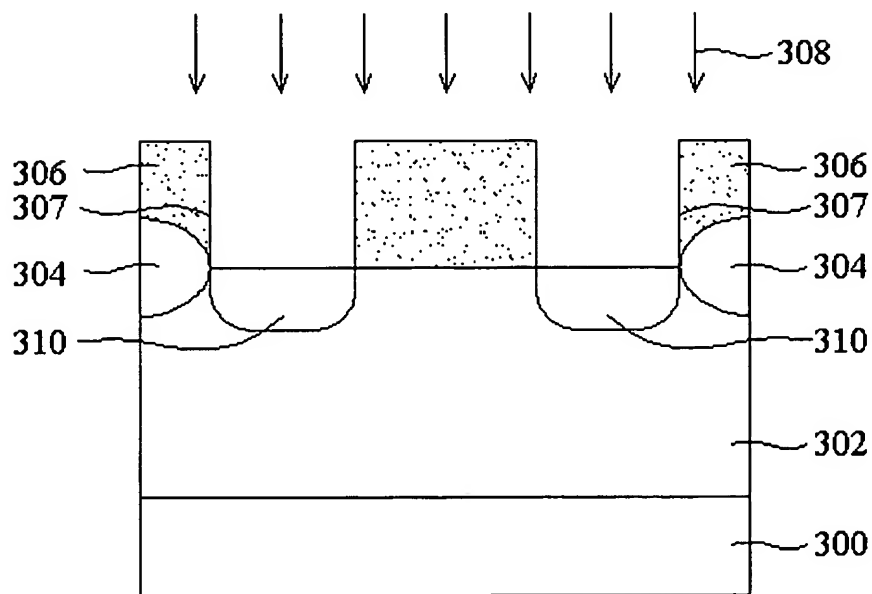


FIG. 3a

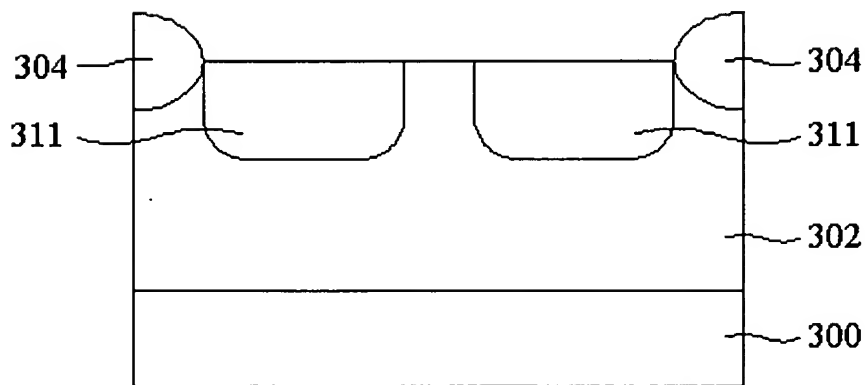


FIG. 3b

HIGH-VOLTAGE MOS TRANSISTOR AND METHOD FOR FABRICATING THE SAME

FIG. 3e